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WHAT IS CLAIMED:

1. A circuit, comprising:

a charge-pump operable to supply an output voltage; and

a current mirror responsive to the output voltage of the charge-pump, and operable to output a relatively constant current and suppress noise from the output voltage.

2. The circuit of claim 1, further comprising:

a filter arranged between the charge-pump and the current mirror, the filter operable to further suppress noise from the output voltage.

3. The circuit of claim 2, wherein the filter includes a bypass capacitance.

4. The circuit of claim 2, wherein the current mirror is operable to isolate the filter from a load circuit in communication with the current mirror.

5. The circuit of claim 4, wherein the load circuit includes a regulator loop operable to generate a consistent output voltage.

6. The circuit of claim 4, wherein the load circuit includes a voltage reference generator operable to generate a reference voltage.

7. The circuit of claim 4, wherein the load circuit includes

a voltage controlled oscillator operable to generate an output signal having a pre-determined oscillation frequency.

8. The circuit of claim 1, wherein the current mirror is operable to reject variations in the output voltage of the charge-pump.

9. The circuit of claim 1, further comprising:

a plurality of charge-pumps operable to supply an output voltage, and wherein the current mirror is operable to suppress noise from the output voltage of the plurality of charge-pumps.

10. The circuit of claim 9, wherein the current mirror is operable to reject variations in the output voltage of the plurality of charge-pumps.

11. The circuit of claim 9, further comprising:

one or more filters arranged between the plurality of charge-pumps and the current mirror, the one or more filters operable to further suppress noise from the output voltage of the plurality of charge-pumps.

12. The circuit of claim 11, wherein at least one of the one or more filters includes a bypass capacitance.

13. The circuit of claim 11, further comprising:

a plurality of current mirrors, each current mirror operable to provide a constant current to a corresponding load

circuit and suppress noise from a corresponding output voltage of a charge-pump.

14. The circuit of claim 13, wherein at least one of the plurality of load circuits includes a regulator loop operable to generate a consistent output voltage.

15. The circuit of claim 13, wherein at least one of the plurality of load circuits includes a voltage reference generator operable to generate a reference voltage.

16. The circuit of claim 13, wherein at least one of the plurality of load circuits includes a voltage controlled oscillator operable to generate an output signal having a pre-determined oscillation frequency.

17. A circuit, comprising:

supply means for supplying an output voltage; and

suppression means for suppressing noise from the supplied output voltage and converting the supplied output voltage into a relatively constant current.

18. The circuit of claim 17, further comprising:

filtering means arranged between the supply means and the suppression means, the filtering means for further suppressing noise from the supplied output voltage.

19. The circuit of claim 18, wherein the filtering means

includes a bypass capacitance means.

20. The circuit of claim 18, wherein the suppression means isolates the filtering means from a loading means in communication with the suppression means.

21. The circuit of claim 20, wherein the loading means includes a regulator loop means for generating a consistent output voltage.

22. The circuit of claim 20, wherein the loading means includes a voltage reference generator means for generating a reference voltage.

23. The circuit of claim 20, wherein the loading means includes a voltage controlled oscillator means for generating an output signal having a pre-determined oscillation frequency.

24. The circuit of claim 17, wherein the suppression means rejects variations in the supplied output voltage.

25. The circuit of claim 17, wherein the supply means includes a charge-pump means.

26. The circuit of claim 17, wherein the supply means includes a plurality of charge-pump means.

27. The circuit of claim 26, wherein the suppression means rejects voltage variations from an output voltage of the

plurality of charge-pump means.

28. The circuit of claim 26, further comprising:

a filtering means including one or more filter means arranged between the plurality of charge-pump means and the suppression means, the one or more filter means for further suppressing noise from the output voltage of the plurality of charge-pump means.

29. The circuit of claim 28, wherein at least one of the one or more filter means includes a bypass capacitance means.

30. The circuit of claim 28, wherein the suppression means includes a plurality of current mirror means in communication with a plurality of loading means, each current mirror means for providing a relatively constant current source to a corresponding loading means and suppressing noise from an output voltage of a corresponding charge-pump means.

31. The circuit of claim 30, wherein the plurality of loading means includes a plurality of regulator loop means for generating a consistent output voltage.

32. The circuit of claim 30, wherein the plurality of loading means includes a plurality of voltage reference generator means for generating a reference voltage.

33. The circuit of claim 30, wherein the plurality of loading

means includes a plurality of voltage controlled oscillator means for generating an output signal having a pre-determined oscillation frequency.

34. A method of suppressing noise, comprising:

providing an output voltage having an associated noise component; and

suppressing the noise component in the output voltage including supplying a relatively constant current in response to the output voltage.

35. The method of claim 34, further comprising:

filtering the output voltage with a filtering apparatus to further suppress the noise component.

36. The method of claim 35, wherein filtering the output voltage includes filtering the output voltage with a bypass capacitance.

37. The method of claim 35, further comprising:

isolating the filter apparatus from a load circuit receiving the relatively constant current source.

38. The method of claim 37, wherein isolating the filter apparatus includes isolating the filter apparatus from a regulator loop operable to generate a consistent output voltage.

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39. The method of claim 37, wherein isolating the filter apparatus includes isolating the filter apparatus from a voltage reference generator operable to generate a reference voltage.

40. The method of claim 37, wherein isolating the filter apparatus includes isolating the filter apparatus from a voltage controlled oscillator operable to generate an output signal having a pre-determined oscillation frequency.

41. The method of claim 34, wherein suppressing the noise component includes rejecting variations in an output voltage of a charge-pump.

42. The method of claim 34, further comprising:

providing an output voltage to a load circuit from a plurality of charge-pumps, and suppressing noise from the output voltage of the plurality of charge-pumps.

43. The method of claim 42, wherein suppressing the noise component includes rejecting variations in the output voltage of the plurality of charge-pumps.

44. The method of claim 43, further comprising:

filtering the output voltage to suppress noise including providing a plurality of filters arranged between the plurality of charge-pumps and the current mirror.

45. The method of claim 44, wherein filtering the output voltage to suppress noise includes providing a bypass capacitance.

46. The method of claim 44, further comprising:

suppressing a noise component in the output voltage including providing a plurality of current mirrors that are operable to supply a relatively constant current to a like plurality of load circuits.

47. The method of claim 46, wherein:

supplying the relatively constant current includes supplying the relatively constant current to a plurality of regulator loops, each regulator loop operable to generate a consistent output voltage.

48. The method of claim 46, wherein:

supplying the relatively constant current includes supplying the relatively constant current to a plurality of voltage reference generators, each voltage reference generator operable to generate a reference voltage.

49. The method of claim 46, further comprising:

supplying the relatively constant current includes supplying the relatively constant current to a plurality of voltage controlled oscillators, each voltage controlled oscillator operable to generate an output signal having a

pre-determined oscillation frequency.

50. An Ethernet transceiver, comprising:

a transmitter;

a receiver;

a charge-pump operable to supply an output voltage to a current mirror;

the current mirror arranged between the charge-pump and a voltage regulator, the current mirror operable to supply a relatively constant current to the voltage regulator and suppress noise from the output voltage of the charge-pump; and

the voltage regulator further in communication with at least one of the transmitter and the receiver, the voltage regulator operable to provide a relatively constant voltage to the at least one of the transmitter and the receiver.

51. The Ethernet transceiver of claim 50, further comprising:

a filter arranged between the charge-pump and the current mirror, the filter operable to further suppress noise from the output voltage of the charge-pump.

52. The Ethernet transceiver of claim 51, wherein the filter includes a bypass capacitance.

53. The Ethernet transceiver of claim 51, wherein the current mirror is operable to isolate the filter from the voltage regulator.

54. The Ethernet transceiver of claim 53, wherein the voltage regulator includes a regulator loop operable to generate a consistent output voltage.

55. The Ethernet transceiver of claim 53, wherein the voltage regulator includes a voltage reference generator operable to generate a reference voltage.

56. The Ethernet transceiver of claim 53, wherein the voltage regulator includes a voltage controlled oscillator operable to generate an output signal having a pre-determined oscillation frequency.

57. The Ethernet transceiver of claim 50, wherein the current mirror is operable to reject variations in the output voltage of the charge-pump.

58. The Ethernet transceiver of claim 50, further comprising:

a plurality of charge-pumps operable to supply an output voltage, the current mirror operable to suppress noise from the output voltage of the plurality of charge-pumps.

59. The Ethernet transceiver of claim 58, further comprising:

one or more filters arranged between the plurality of charge-pumps and the current mirror, the one or more filters operable to further suppress noise from the output voltage of the plurality of charge-pumps.

60. The Ethernet transceiver of claim 59, wherein at least one of the one or more filters includes a bypass capacitance.

61. The Ethernet transceiver of claim 58, wherein the current mirror is operable to reject variations in the output voltage of the plurality of charge-pumps.

62. The Ethernet transceiver of claim 50, wherein the Ethernet transceiver is compliant with IEEE 1000BaseT.

63. An Ethernet transceiver, comprising:

transmitter means for transmitting a signal;

receiver means for receiving a signal;

supply means for supplying an output voltage to a noise suppression means, the noise suppression means for suppressing noise from the supplied output voltage and converting the supplied output voltage into a relatively constant current, and supplying the relatively constant current to a loading means.

64. The Ethernet transceiver of claim 63, wherein the noise suppression means comprises:

filtering means for further suppressing noise from the supplied output voltage.

65. The Ethernet transceiver of claim 64, wherein the filtering means includes a bypass capacitance means.

66. The Ethernet transceiver of claim 64, wherein noise

suppression means isolates the filtering means from the loading means.

67. The Ethernet transceiver of claim 66, wherein the loading means includes a regulator loop means for generating a consistent output voltage.

68. The Ethernet transceiver of claim 66, wherein the loading means includes a voltage reference generator means for generating a reference voltage.

69. The Ethernet transceiver of claim 66, wherein the loading means includes a voltage controlled oscillator means for generating an output signal having a pre-determined oscillation frequency.

70. The Ethernet transceiver of claim 63, wherein the noise suppression means rejects variations in the supplied output voltage.

71. The Ethernet transceiver of claim 63, wherein the supply means includes a charge-pump means.

72. The Ethernet transceiver of claim 63, wherein the supply means includes a plurality of charge-pump means.

73. The Ethernet transceiver of claim 72, wherein the noise suppression means further includes:

a filtering means including one or more filter means in

communication with the plurality of charge-pump means, the one or more filter means operable to suppress noise from an voltage output of the plurality of charge-pump means.

74. The Ethernet transceiver of claim 73, wherein at least one of the one or more filter means includes a bypass capacitance means.

75. The Ethernet transceiver of claim 72, wherein the noise suppression means rejects voltage variations from the output of the plurality of charge-pump means.

76. The Ethernet transceiver of claim 63, wherein the Ethernet transceiver is compliant with IEEE 1000BaseT.

77. The circuit of claim 3, wherein the bypass capacitance includes a bypass capacitor.

78. The circuit of claim 12, wherein the bypass capacitance includes a bypass capacitor.

79. The circuit of claim 19, wherein the bypass capacitance means includes a bypass capacitor means.

80. The circuit of claim 29, wherein the bypass capacitance means includes a bypass capacitor means.

81. The method of claim 36, wherein filtering the output voltage includes filtering the output voltage with a bypass

capacitor.

82. The method of claim 45, wherein providing a bypass capacitance includes providing a plurality of bypass capacitors.

83. The Ethernet transceiver of claim 52, wherein the bypass capacitance includes a bypass capacitor.

84. The Ethernet transceiver of claim 60, wherein the bypass capacitance includes a bypass capacitor.

85. The Ethernet transceiver of claim 65, wherein the bypass capacitance means includes a bypass capacitor means.

86. The Ethernet transceiver of claim 74, wherein the bypass capacitance means includes a bypass capacitor means.